

**SYNCHRONISM PHASE-SWITCHING CIRCUIT
FOR THE RECOVERY OF RECEIVED DATA**

Abstract of the Disclosure

A switching circuit is for switching an output thereof to one of a plurality of N input clock signals which are delayed relative to one another. The
5 switching circuit includes at least one circuit responding to a control signal to enable the transmission, on an output signal, of a new signal of the plurality of input signals. The new signal is advanced or delayed relative to a current signal of the
10 plurality of input signals which is currently transmitted on the output signal. The at least one circuit enables the transmission of the new signal before disabling the transmission of the current signal on the output signal. This substantially prevents the
15 production of false signals during the switching of the output signal from one of the clock signals to another.

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